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S120	6	(trace tracing) with (entry with exit with call with return)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 13:02
S122	1030	(717/128,130,158).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/06 13:14
S123	26	tail adj merg\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/06 13:15
S124	0	S122 and S123	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/06 13:15
S125	274	(714/35).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/07 13:01
S126	1030	(717/128,130,158).CCLS.	US-PGPUB; USPAT; USOCR	OR	OFF	2007/03/07 13:01
S127	6	(trace tracing probe) with (entry with exit with call with return)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 13:02
S128	22847	(trace tracing probe) with (entry exit call return)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 13:02
S129	1235	S125 S126	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 13:02

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S13 0	344	S128 and S129	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 13:02
S13 1	1193	(trace tracing probe) with (entry exit call return) and tail	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 13:03
S13 2	23	S129 and S131	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/07 13:03


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 Terms used **tail merge instrumentation instrument probe**

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1 [A new framework for debugging globally optimized code](#)



Le-Chun Wu, Rajiv Mirani, Harish Patil, Bruce Olsen, Wen-mei W. Hwu

 May 1999 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1999 conference on Programming language design and implementation PLDI '99**, Volume 34 Issue 5

Publisher: ACM Press

Full text available: pdf(1.54 MB)

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With an increasing number of executable binaries generated by optimizing compilers today, providing a clear and correct source-level debugger for programmers to debug optimized code has become a necessity. In this paper, a new framework for debugging globally optimized code is proposed. This framework consists of a new code location mapping scheme, a data location tracking scheme, and an emulation-based forward recovery model. By taking over the control early and emulating instructions selective ...

2 [Software Pipelining Irregular Loops On the TMS320C6000 VLIW DSP Architecture](#)



Elana Granston, Eric Stotzer, Joe Zbiciak

 August 2001 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN workshop on Languages, compilers and tools for embedded systems LCTES '01 , Proceedings of the 2001 ACM SIGPLAN workshop on Optimization of middleware and distributed systems OM '01**, Volume 36 Issue 8

Publisher: ACM Press

Full text available: pdf(88.54 KB)

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The TMS320C6000 architecture is a leading family of Digital Signal Processors (DSPs). To achieve peak performance, this VLIW architecture relies heavily on software pipelining. Traditionally, software pipelining has been restricted to *regular* (FOR) loops. More recently, software pipelining has been extended to *irregular* (WHILE) loops, but only on architectures that provide special-purpose hardware such as rotating (predicate and general-purpose) register files, specific instruct ...

Keywords: VLIW architectures, WHILE loops, digital signal processors, software pipelining

3 [Assembly instruction level reverse execution for debugging](#)



Tankut Akgul, Vincent J. Mooney III

 April 2004 **ACM Transactions on Software Engineering and Methodology (TOSEM)**,



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IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

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Dec. 2006 Page(s):65 - 76
Digital Object Identifier 10.1109/MICRO.2006.34